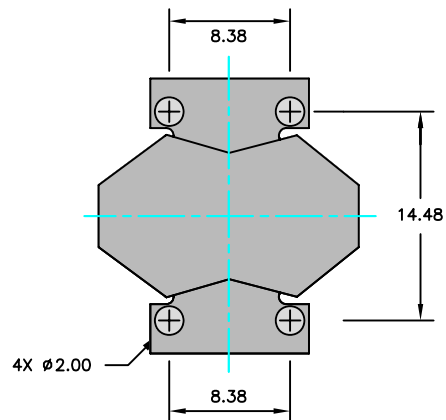
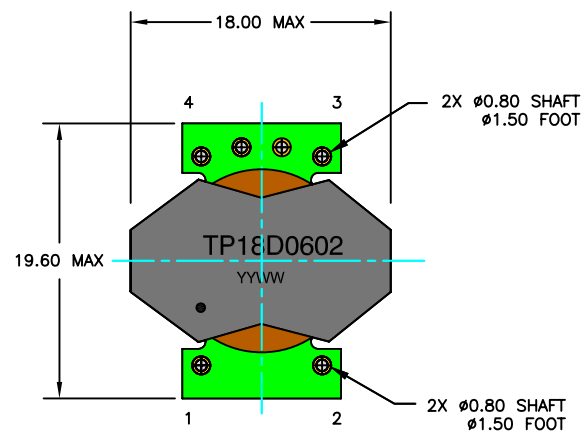
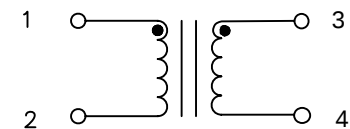
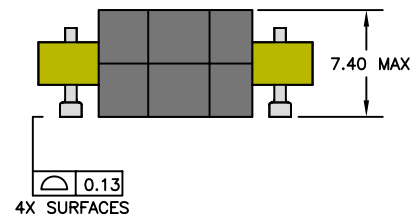




SUGGESTED PAD LAY-OUT



SCHEMATICS

#### TEST NOTES:

UNLESS OTHERWISE SPECIFIED, TESTING IS PERFORMED AT 100 KHz, 100 mV, 25°C ±5°C.

1. TURNS RATIO AND POLARITY: (100% TEST)  
 $(1-2)/(3-4) = 3.0 \pm 2\%$
2. INDUCTANCE: (100% TEST)  
 $(1-2) = 48.0 \mu\text{H MIN}$
3. LEAKAGE INDUCTANCE: (SAMPLE TEST)  
 $(1-2) \text{ WITH } (3-4) \text{ SHORTED} = 0.85 \mu\text{H MAX.}$
4. RDC: (SAMPLE TEST)  
 $(1-2) = 43.0 \text{ mOHMS MAX.}$   
 $(3-4) = 3.00 \text{ mOHMS MAX.}$
5. HIPOT: (100% TEST)

1500 VDC PIN 1 TO PIN 3  
500 VDC PINS 1,3 TO CORE.

Distributed By



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UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN MILLIMETERS TOLERANCES ARE:

DECIMALS ANGLES  
XX.X ±.25 ± 1°  
XX.XX ±.05  
DO NOT SCALE DRAWING

|       |      |           |                      |       |        |
|-------|------|-----------|----------------------|-------|--------|
| SIZE  | A3   | DWG. NO.  | TP18D0602 (PA0030NL) | REV.  | 11     |
| SCALE | NONE | CADD FILE | TP18D0602-4          | SHEET | 4 OF 4 |